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FIG. 1

Set (Information)		a(001)	b(010)	c(011)	d(100)	e(101)	f(110)	g(111)
Received signal patterns	Transmission word	001110	010101	011011	100011	101101	110110	111000
	Error patterns	101110	110101	111011	000011	001101	010110	011000
		011110	000101	001011	110011	111101	100110	101000
		000110	011101	010011	101011	100101	111110	110000
		001010	010001	011111	100111	101001	110010	111100
		001100	010111	011001	100001	101111	110100	111010
		001111	010100	011010	100010	101100	110111	111001
		101010	110001	111111	000111	001001	010010	011100

FIG. 2

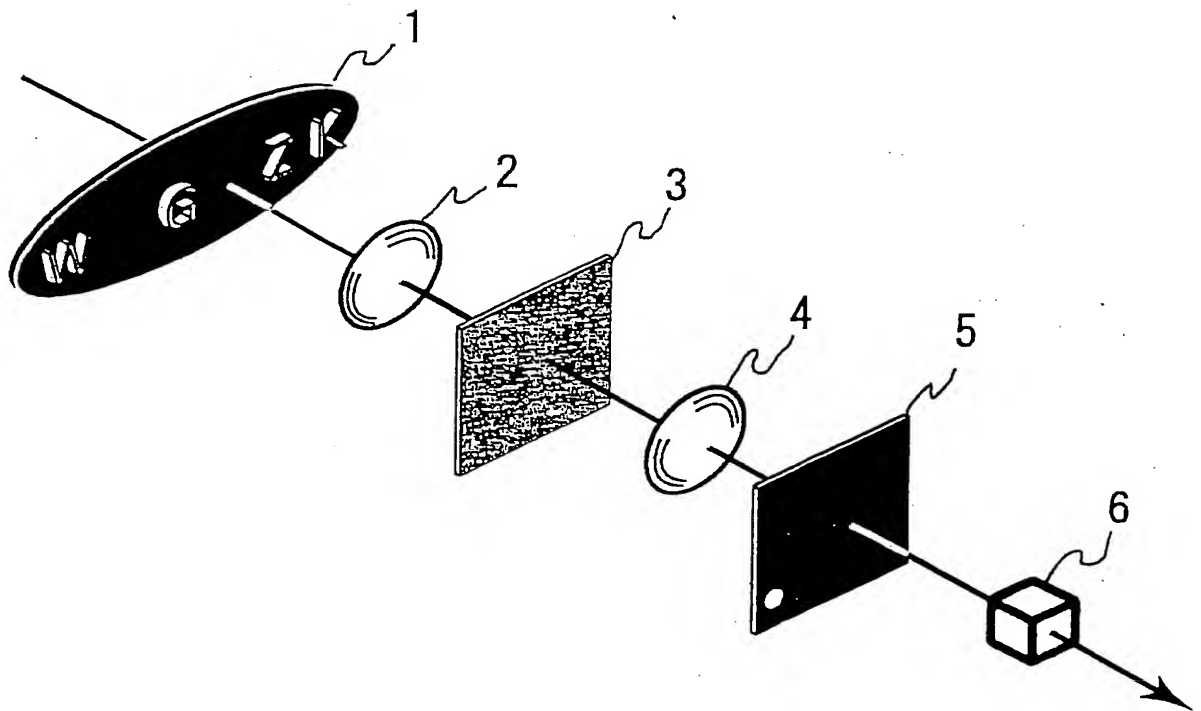


FIG. 3

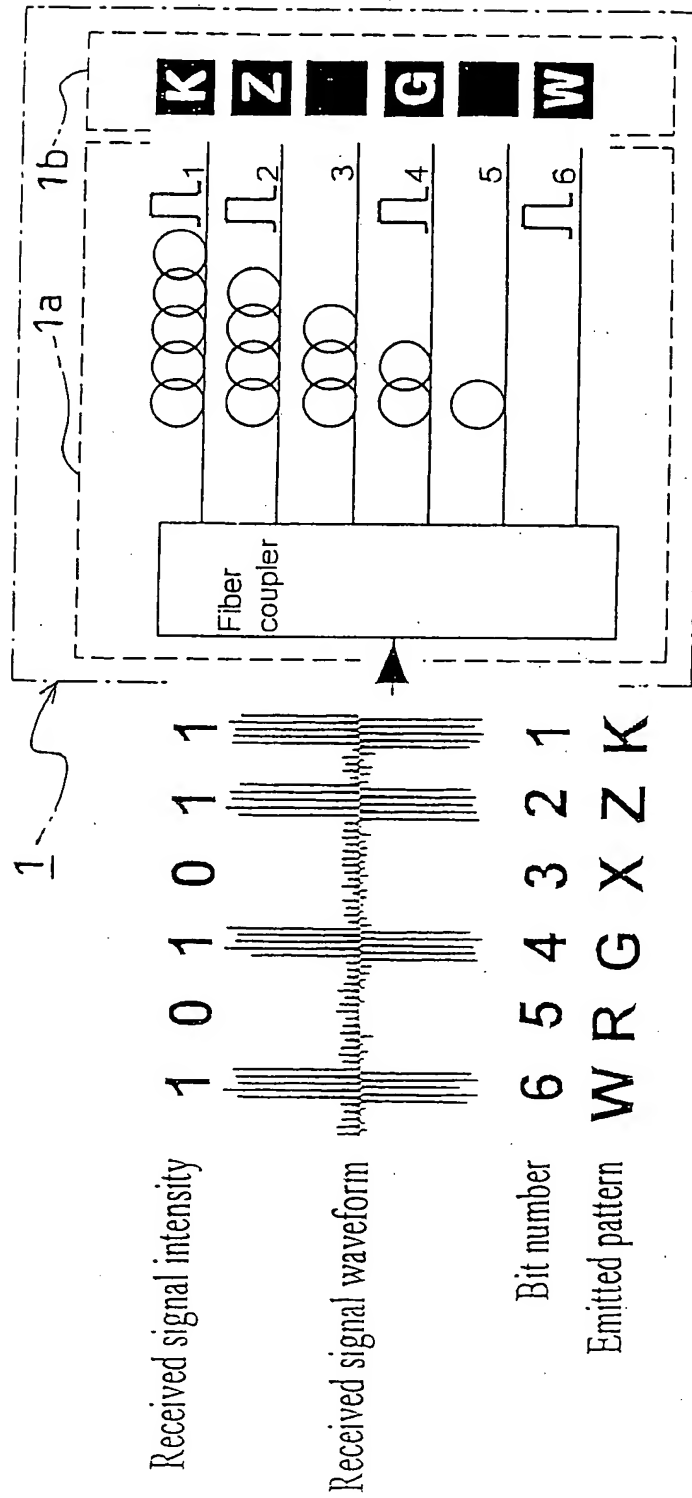


FIG. 4

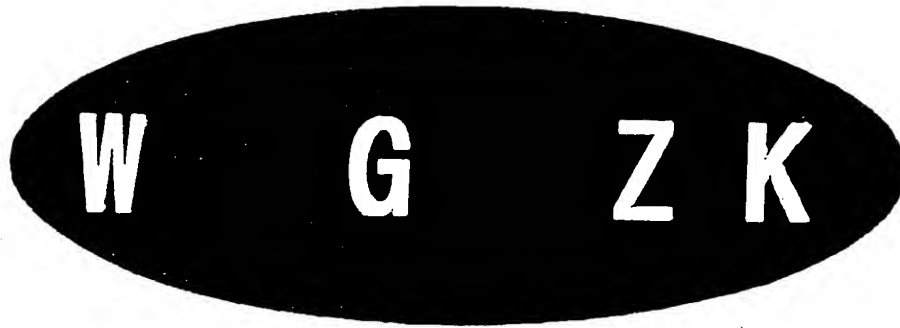


FIG. 5

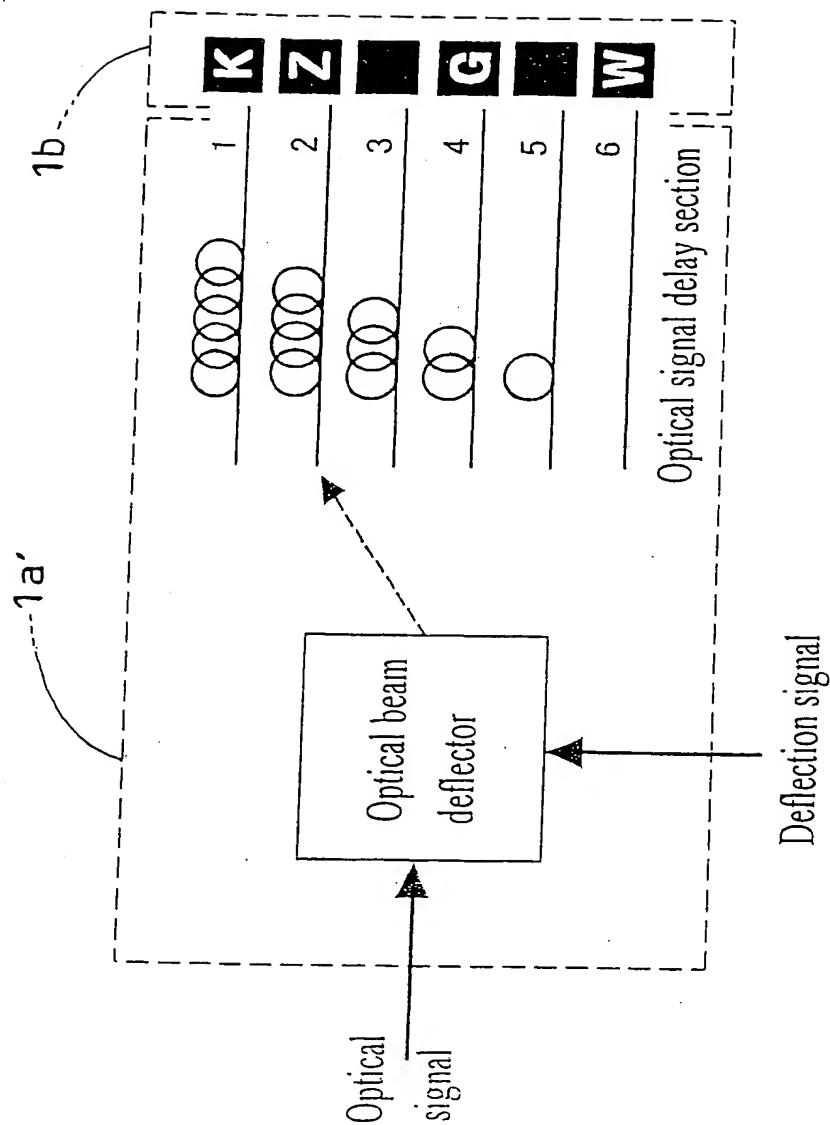


FIG. 6

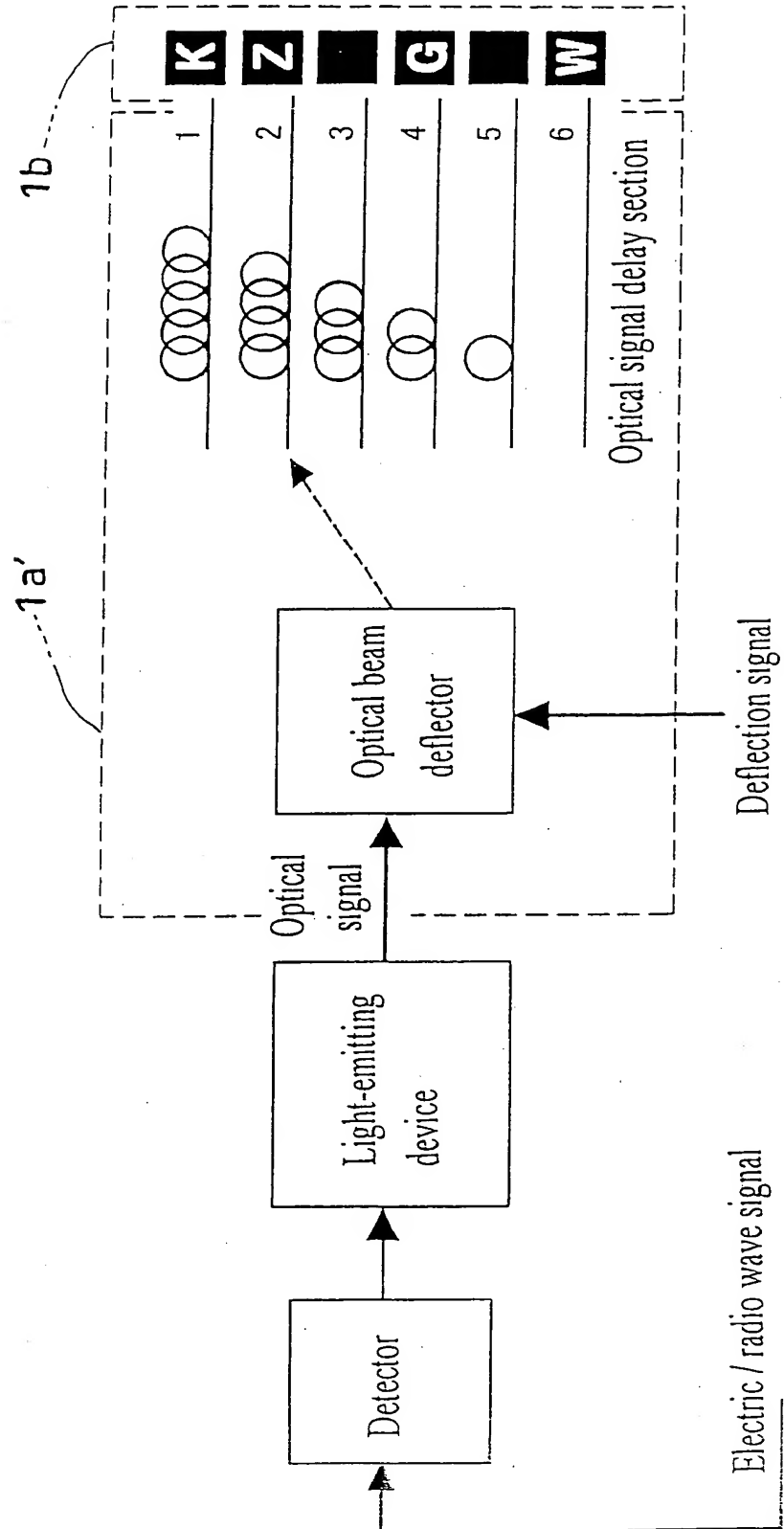


FIG. 7

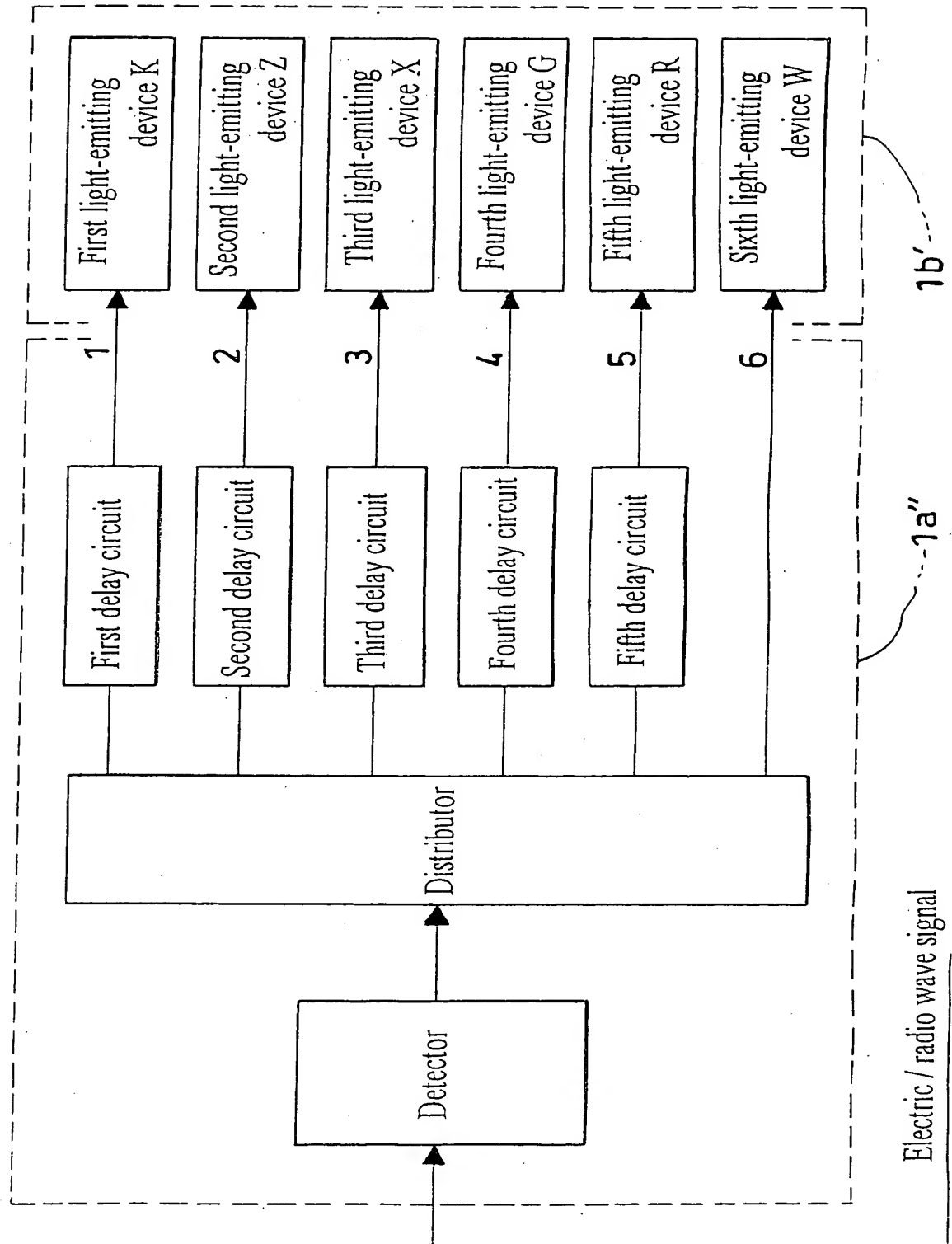


FIG. 9

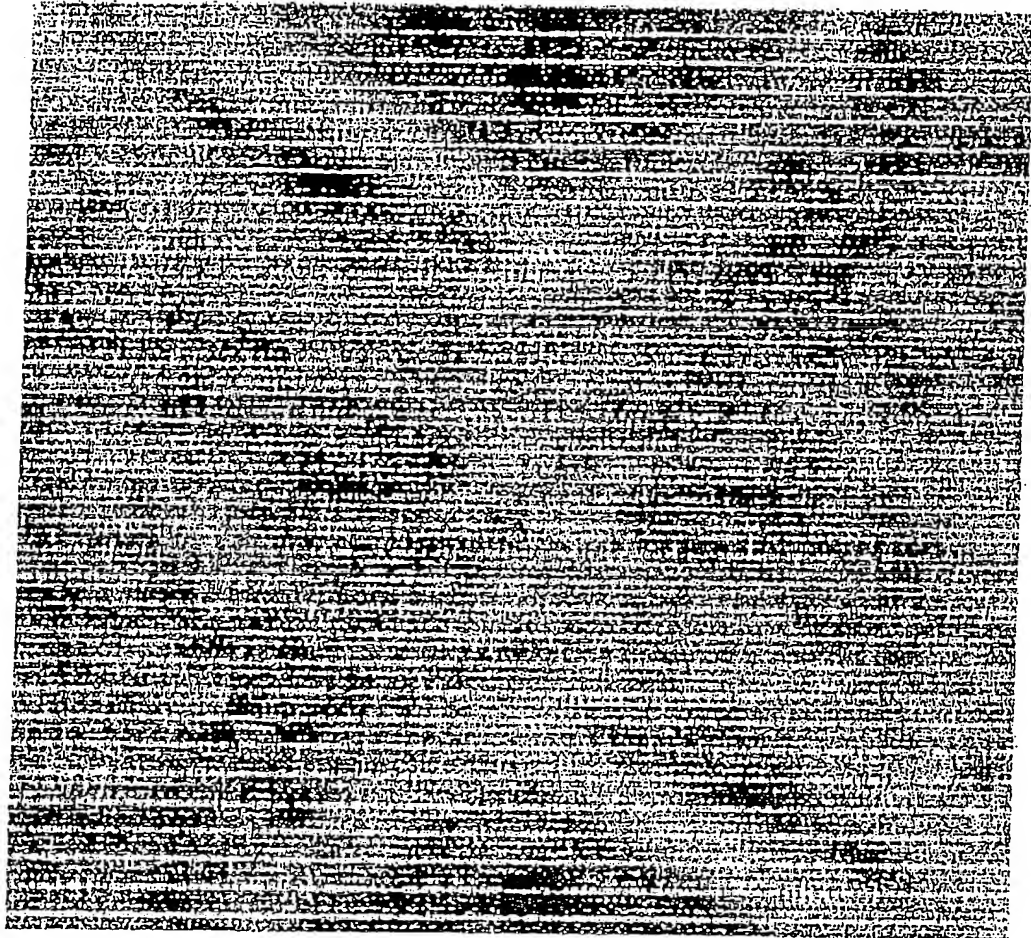


FIG. 10

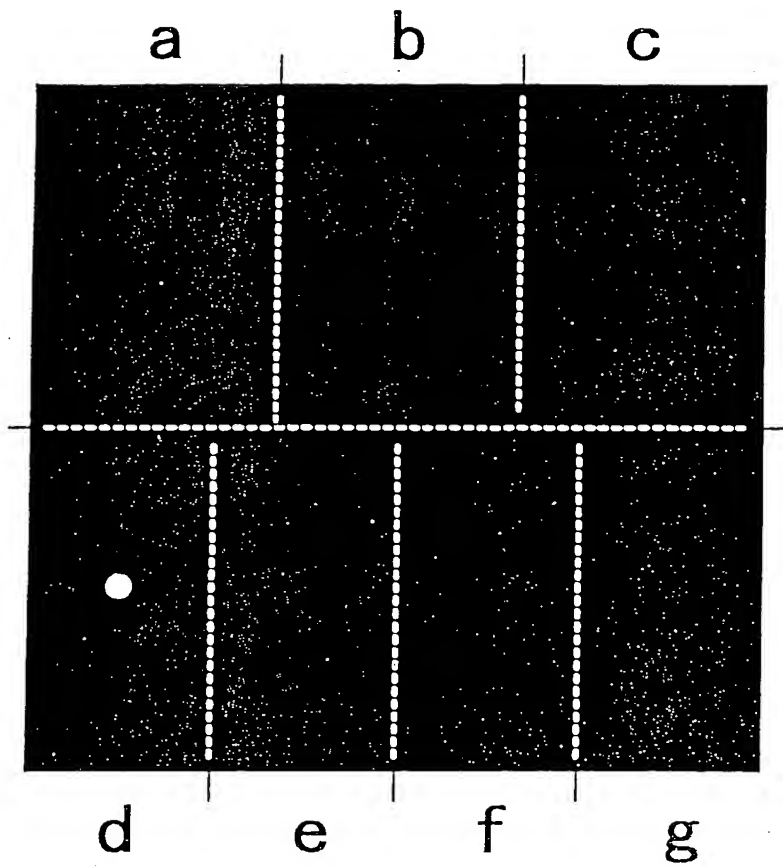


FIG. 11

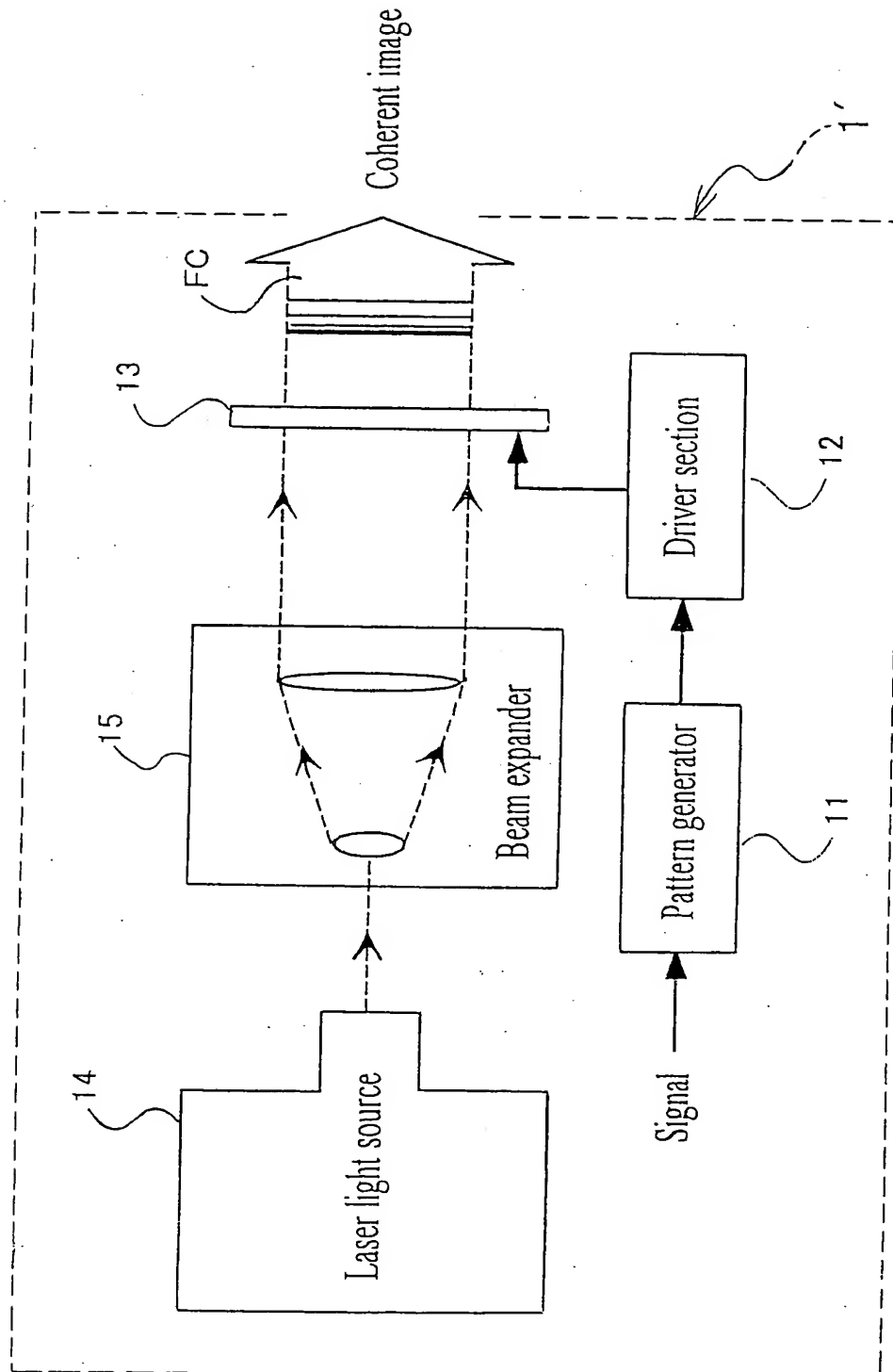


FIG. 12

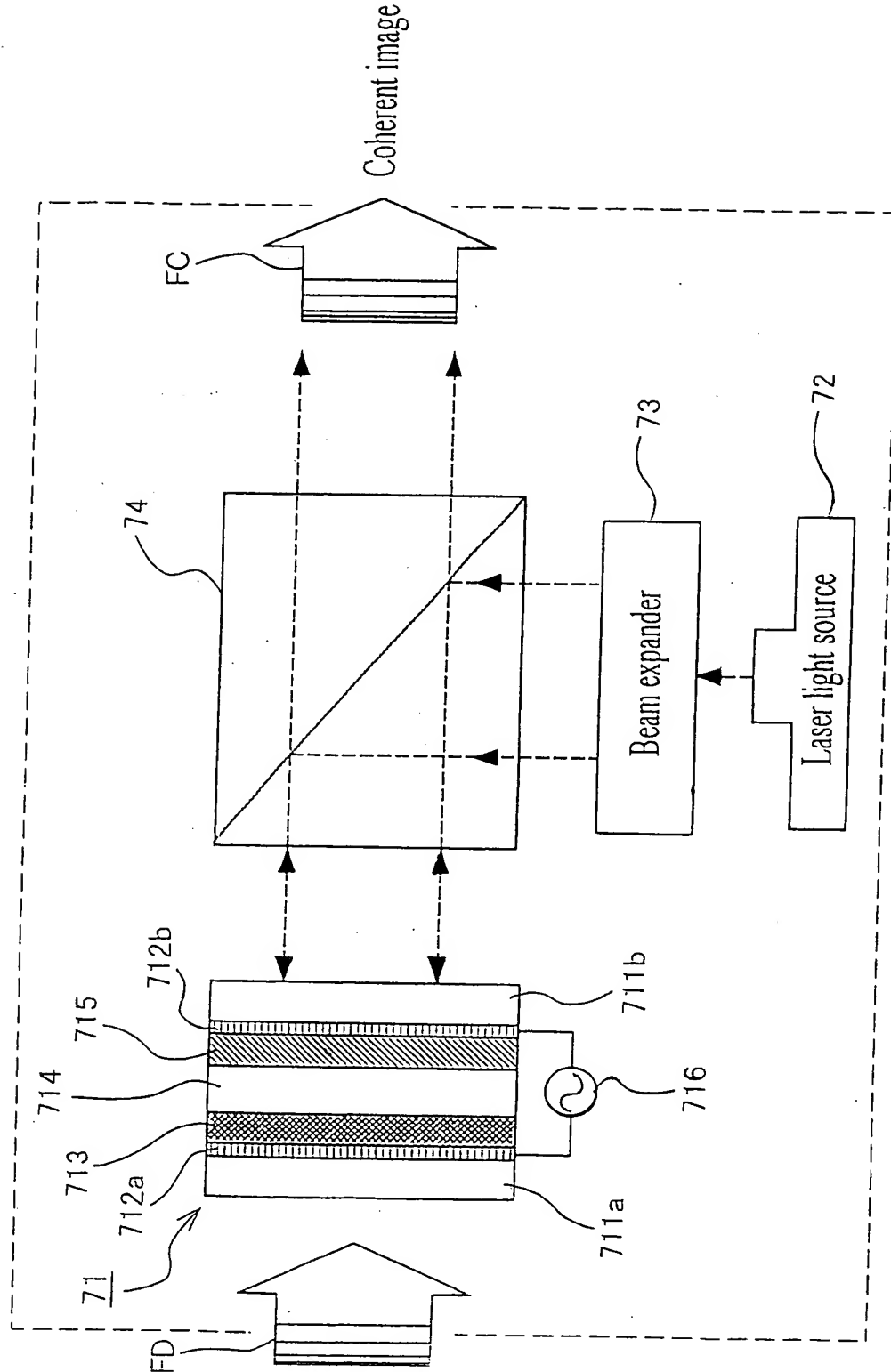


FIG. 13(a)

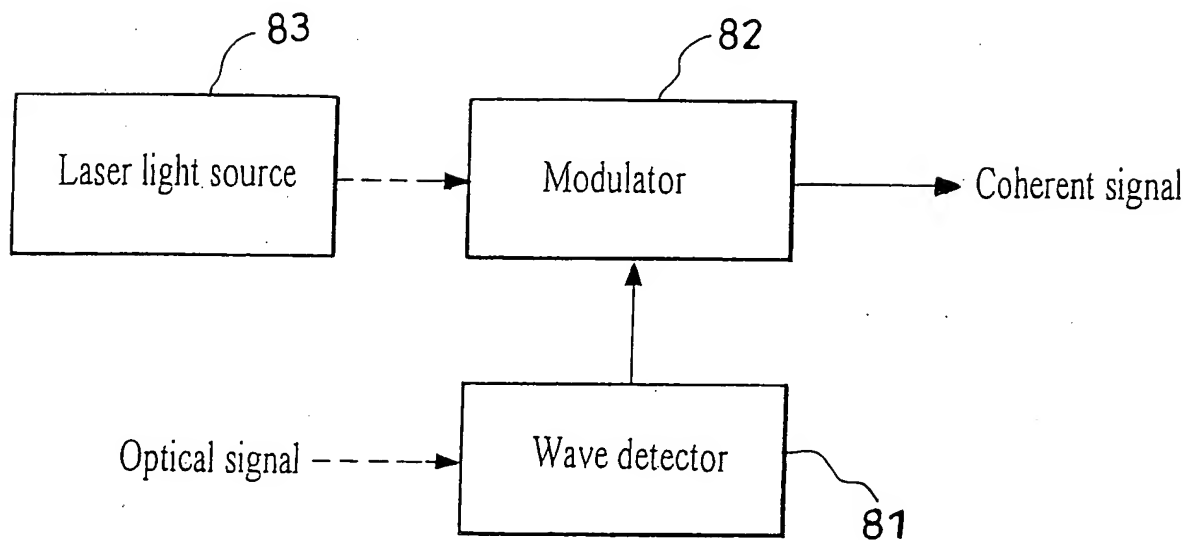


FIG. 13(b)

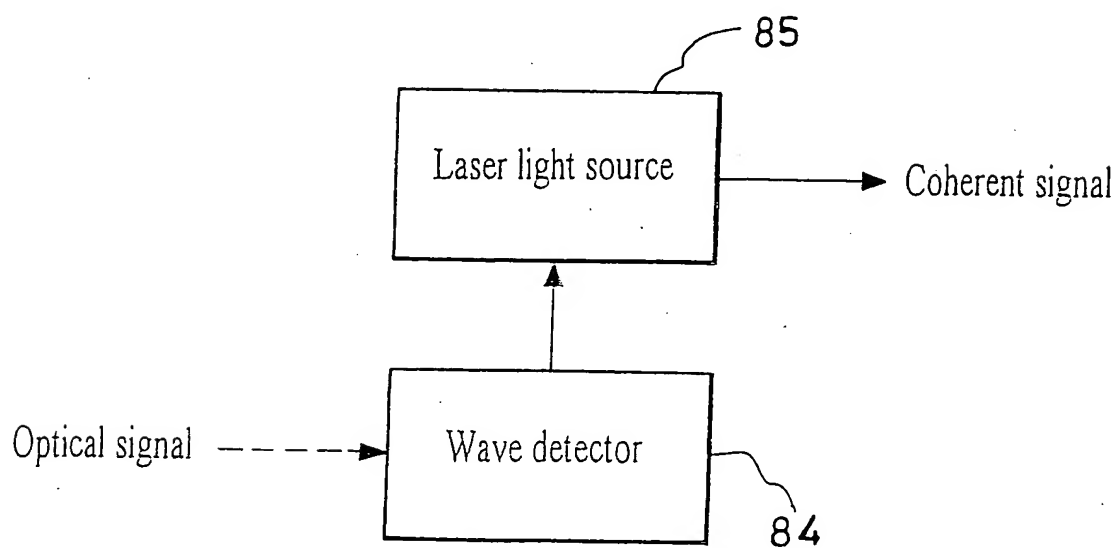


FIG. 14

